

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of Bryant et al.
Serial No. 09/886,823
Filed: June 21, 2001
Title: Double Gated Transistor and Method of
Fabrication

Docket: BUR920000059US1
Date: March 26, 2002
Art Unit: 2823
Examiner: Pham, Long.
Fax: 703-746-4082

Patent

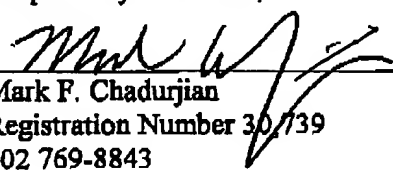
#3/Election
A. K. A.
4/10/02Response to Restriction Requirement

Commissioner of Patents & Trademarks
Washington, D.C. 20231

Sir:

In response to the Restriction Requirement of March 14, 2002, the applicant hereby elects Group II, Claims 25-40, for continued prosecution. This election is made without traverse and without prejudice against the filing of a divisional application based on the unelected claims.

Respectfully submitted,


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cc: Kenneth C. Booth - Schmeiser, Olsen & Watts